



30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D T _C = +25°C
-30V	$25m\Omega$ @ $V_{GS} = -10V$	-27A
-307	38mΩ @ V _{GS} = -4.5V	-22A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- DC-DC Converters
- · Power Management Functions

Features

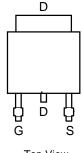
- 100% Unclamped Inductive Switch (UIS) Test In Production
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Note 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

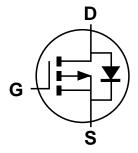
- Case: TO252
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (23)
- Weight: 0.33 grams (Approximate)



Top View



Top View Pin-Out



Equivalent Circuit

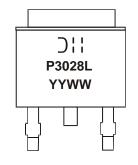
Ordering Information (Notes 4)

_			
	Product	Case	Packaging
	DMP3028LK3-13	TO252	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html .

Marking Information



>\|\ = Manufacturer's Marking
P3028L= Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 14 = 2014)
WW = Week (01 - 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	-30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 6) V	Steady $T_C = +25^{\circ}C$ State $T_C = +70^{\circ}C$			-27 -22	А
Continuous Drain Current (Note 6) V _{GS} = -10V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-11 -8.6	А
Maximum Body Diode Continuous Current	Is	-2.5	Α		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	-40	А
Avalanche Current (Note 7) L = 0.1mH			las	-22	Α
Avalanche Energy (Note 7) L = 0.1mH			Eas	24	mJ

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = +25°C	D-	1.6	W
Total Power Dissipation (Note 5)	T _A = +70°C	P_D	1.0	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	D	77	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	34	
Total Power Dissipation (Note 6)	$T_A = +25$ °C	D-	2.8	W
Total Fower Dissipation (Note o)	$T_A = +70^{\circ}C$	P_D	1.8	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	D	45	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	29	
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	4.5		
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-1	μA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	-1	1	-2.4	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	D		20	25	mΩ	$V_{GS} = -10V, I_D = -7A$	
Static Dialit-Source Off-Resistance	R _{DS(ON)}	_	29	38	11122	$V_{GS} = -4.5V$, $I_D = -6.2A$	
Diode Forward Voltage	V _{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -2.1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	1	1241	_	pF	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Output Capacitance	Coss		147	_	pF	$V_{DS} = -15V, V_{GS} = 0V$ of = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	110	_	pF	1 - 1.00112	
Gate Resistance	R_{G}	_	15	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -10V)	Qg	_	22	_	nC		
Total Gate Charge (V _{GS} = -4.5V)	Qg		11	_	nC	- Vps = -15V. ID = -7A	
Gate-Source Charge	Qgs	_	3.5	_	nC	VDS = -15V, ID = -7A	
Gate-Drain Charge	Q _{gd}		4.7	_	nC		
Turn-On Delay Time	t _{D(on)}		9.7	_	ns		
Turn-On Rise Time	tr		17.1	_	ns	$V_{GS} = -10V, V_{DD} = -15V,$	
Turn-Off Delay Time	t _{D(off)}	_	60.5	_	ns	$R_{GEN} = 6\Omega$ $I_D = -7A$	
Turn-Off Fall Time	t _f	_	40.4	_	ns	71010	

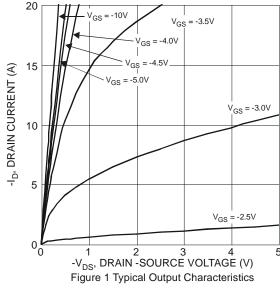
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

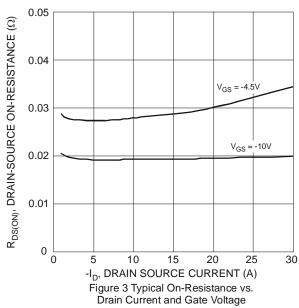
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = 25^{\circ}C$.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.







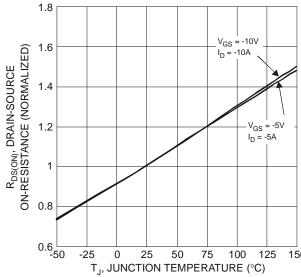
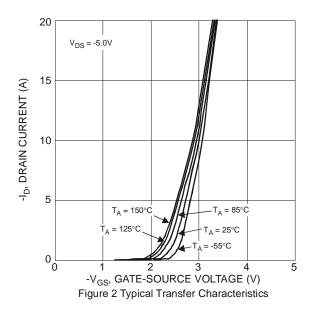
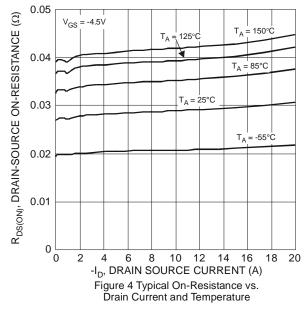
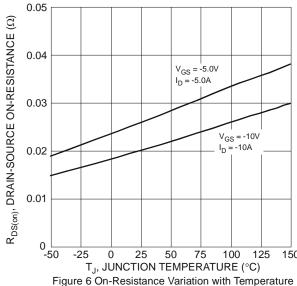


Figure 5 On-Resistance Variation with Temperature









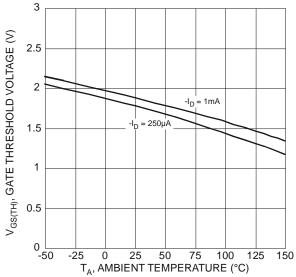


Figure 7 Gate Threshold Variation vs. Ambient Temperature

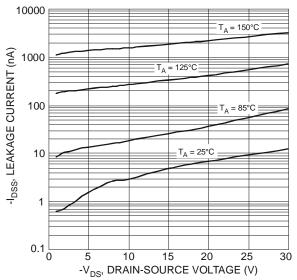
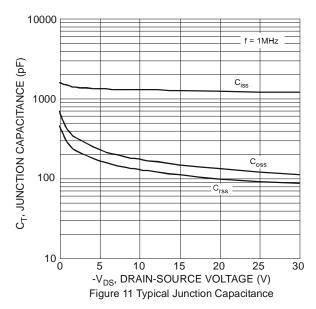
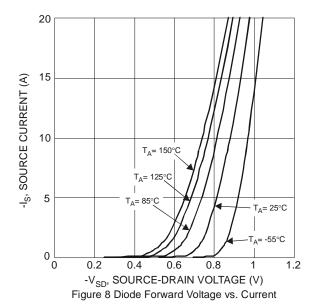
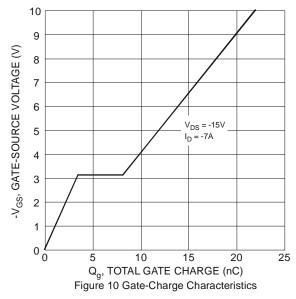
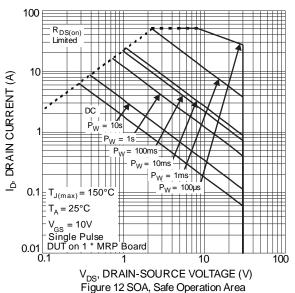


Figure 9 Typical Drain-Source Leakage Current vs. Voltage











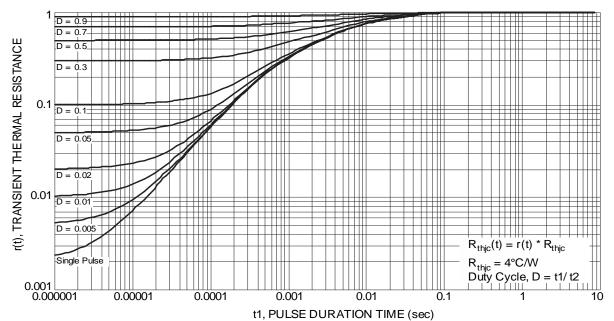
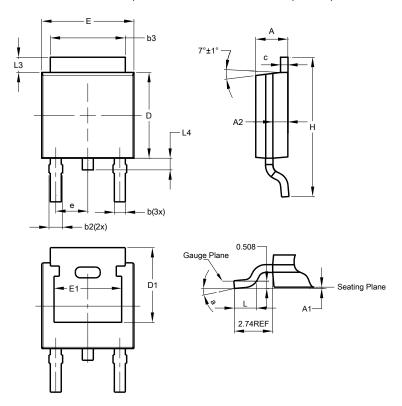


Figure 13 Transient Thermal Resistance



Package Outline Dimensions

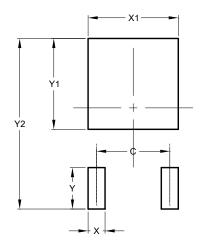
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



TO252 (DPAK)					
Dim Min		Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
p	0.64	0.88	0.783		
b2 0.76		1.14	0.95		
b3 5.21		5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е -		-	2.286		
П	6.45	6.70	6.58		
E1	4.32	-	-		
I	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
C	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		



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