# HZ0805C202R-10

#### PHYSICAL DIMENSIONS:

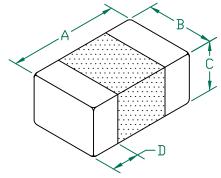
IMPEDANCE (Q)

A 2.00 [.079]  $\pm$  0.20 [.008]

B 1.25 [.049] ± 0.20 [.008]

 $C 0.90 [.035] \pm 0.20 [.008]$ 

D 0.51 [.020] ± 0.25 [.010]



ELEC1	ELECTRICAL CHARACTERISTICS:							
Z @ 100M ( Ω )	Z @ 100MHz $\left(\begin{array}{c}\Omega\end{array}\right)$		Rated Current					
Nominal	2000							
Minimum	1500							
Maximum	2500	0.500	300 mA					

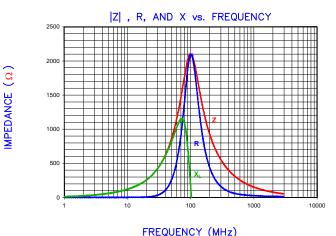
NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C (INCLUDING SELF-HEATING)

#### 

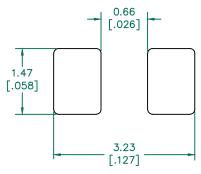
Z vs FREQUENCY
IMPEDANCE UNDER DC BIAS

#### FREQUENCY (MHz)

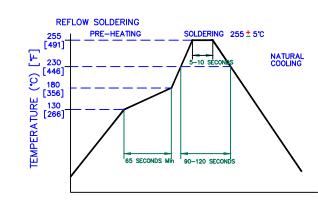


AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3228

### LAND PATTERNS FOR REFLOW SOLDERING



## (For wave soldering, add 0.763 ([.030] to this dimension.)



RECOMMENDED SOLDERING CONDITIONS

DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird									
				Tech, and is loaned in confidence subject to return upon request and		▎█▗▄▝▋▃▗▄█▘ ▏							
				with the understanding that no			Laird						
				copies shall be made without the		Lanu							
				written consent of Laird Tech. Al	'								
				reserved.									
n	ADD OPERATING TEMPERATURE	08/05/13	OI.	PROJECT/PART NUMBER:	T,	EV	PART T	PE:	DRAWN BY:				
	UPDATE LAIRD LOGO AND REFLOW CURVE			HZ0805C202R-10		D	CO-FIRE		тмв				
C	CHANGE TO PAPER TAPE	03/03/10	ÿ	1120000020211 10	П	_			1,,,,,				
В	UPDATE COMPANY LOGO ADD ROHS	03/20/08	JRK	DATE: 04/03/04	CAL	E: N	TS	SHEET:					
Α	ORIGINAL DRAFT	04/03/04	ТМВ	CAD #	OOL								
REV	DESCRIPTION	DATE	INT	"HZ0805C202R-10-D		•	-	1	of 1				